

1 **ABSTRACT OF THE DISCLOSURE**

2 A video sampling circuit of a liquid crystal display
3 with a merit of feed-through voltage reduction was
4 disclosed. The sampling circuit comprises a first thin
5 film transistor (TFT) and a counteracting device. The
6 first TFT has a first electrode to receive the analog
7 signal, a control electrode to receive the clock signal,
8 and a second electrode, and samples the analog signal
9 when the clock signal is at a first logic level. The
10 counteracting device is coupled to the second electrode.
11 When the clock signal is changed from the first logic
12 level to a second logic level, a feed-through voltage
13 drop caused by a parasitic capacitor between the second
14 electrode and the control electrode of the first TFT is
15 reduced.